

Course Title	Advanced Computer Organization & Architecture				
Course Code	ECE305				
Course Type	Elective				
Level	Bachelor (1st Cycle)				
Year / Semester	3 rd Year / 8 th Semester				
Teacher's Name	TBA				
ECTS	6	Lectures / week	3 hours / 14 weeks	Laboratories / week	N/A
Course Purpose and Objectives	<p>The aim of this course is to build on the fundamental concepts of computer organization and architecture and provide students with a solid understanding of the concepts and considerations related to the design, operation and performance evaluation of high-end, modern computer systems. The course follows an embedded laboratory approach, where students are required to utilize CPU simulation tools during the implementation of lectures.</p>				
Learning Outcomes	<p>Upon successful completion of this course students should be able to:</p> <ul style="list-style-type: none"> • Utilize effectively measures which allow the quantitative evaluation of computer systems' performance • Describe the principles and limitations of instruction level parallelism (ILP) • Describe ILP applications in high-performance processors such as superscalar execution, branch prediction and multithreading • Define multicore architectures, and critically compare alternative approaches to multicore organization • Describe basic techniques for optimizing the performance of computer memory systems 				
Prerequisites	ECE210	Co-requisites	None		
Course Content	<p><u>Introduction – Overview:</u> Review of the fundamental concepts of computer organization and architecture, overview of the latest trends in computer systems design, quantitative measures for evaluating computer systems performance.</p> <p><u>Pipelining:</u> The basic concept of pipelining, pipeline implementation, hazards in difficulties in pipeline implementation, mitigation techniques in pipeline implementation, pipeline case studies.</p> <p><u>Instruction Level Parallelism (ILP):</u> Basic definitions and limitations of ILP, methods for exploiting ILP, superscalar execution systems, branch prediction, dynamic scheduling, hardware-based speculation, static scheduling, simultaneous multithreading.</p>				

	<p><u>Multicore architectures</u>: Definition of shared-memory architectures, symmetric versus distributed shared-memory architectures, operational and performance issues, multicore processors organization, multicore multi-level caches, coherence schemes, on-chip multicore interconnect, scaling of multicore architectures, multicore case studies, review of large-scale microprocessor system considerations.</p> <p><u>Advanced Topics in Computer Memory Design</u>: Review of basic computer memory definitions and operational principles, the memory hierarchical organization, cache performance optimization, virtual memory optimization, computer memory design case studies.</p> <p>Latest developments in the area of computer architecture and organization.</p>								
Teaching Methodology	Face- to- face								
Bibliography	<p><i>“Computer Architecture: A Quantitative Approach”</i> by John L. Hennessy</p> <p><i>“Digital Design and Computer Architecture”</i> by David Harris and Sarah Harris</p> <p><i>“Computer Organization and Architecture”</i> by William Stallings</p> <p><i>“Inside the Machine: An Illustrated Introduction to Microprocessors and Computer Architecture”</i> by Jon Stokes</p> <p><i>“The Essentials of Computer Organization and Architecture”</i>, by Linda Null and Julia Lobur</p>								
Assessment	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 60%;">Examinations</td> <td style="text-align: center;">70%</td> </tr> <tr> <td>Assignments/Lab</td> <td style="text-align: center;">20%</td> </tr> <tr> <td>Class Participation and Attendance</td> <td style="text-align: center;">10%</td> </tr> <tr> <td></td> <td style="text-align: center;">100%</td> </tr> </table>	Examinations	70%	Assignments/Lab	20%	Class Participation and Attendance	10%		100%
Examinations	70%								
Assignments/Lab	20%								
Class Participation and Attendance	10%								
	100%								
Language	English								