

Course Title	Digital Systems I				
Course Code	ECE200				
Course Type	Compulsory				
Level	Bachelor (1st Cycle)				
Year / Semester	1 <sup>st</sup> Year / 2 <sup>nd</sup> Semester				
Teacher's Name	TBA				
ECTS	6	Lectures / week	3 hours / 14 weeks	Laboratories / week	N/A
Course Purpose and Objectives	<p>The objective of this course is to provide a comprehensive introduction to the fundamental axioms, theories and conventions underlying the operation of digital systems, and to equip students with the necessary skills which will allow them to analyse, design, test, and simulate the operation of basic digital circuits. The course follows an embedded laboratory approach, where students are required to utilize a variety of digital logic design and simulation tools during the implementation of lectures.</p>				
Learning Outcomes	<p>Upon successful completion of this course, students should be able to:</p> <ul style="list-style-type: none"> <li>• Identify a digital system and its main characteristics, and differentiate between digital and analogue systems</li> <li>• Describe the concepts of binary numbers and binary encoding, and perform conversions between binary, decimal, and hex numbers and between binary codes</li> <li>• Perform basic mathematical operations using binary numbers, and design digital systems capable of performing such operations.</li> <li>• Describe theorems and axioms of Boolean Algebra, and utilize them effectively in the process of designing digital systems</li> <li>• Model, analyse, design, test, and simulate the operation of combinational and sequential circuits using analytic and modular methodologies and tools</li> <li>• Explain the concept of memory in digital systems, and design basic memory modules</li> </ul>				
Prerequisites	ECE105 or CSE105	Co-requisites	None		
Course Content	<p><u>History and Overview</u>: History, overview and applications of digital systems, digital signals and analogue signals, basic concept of a digital system and its main characteristics, electrical representation of binary states, implementation of digital systems through digital logic</p>				

circuits, advantages and disadvantages of using digital systems, interface between digital and analogue systems.

Number Systems and Encoding: The binary number system, binary counting, the hexadecimal number system, integer and fixed-point conversions between binary, hexadecimal and decimal numbers, utilization of binary quantities, binary encoding schemes, encoding and decoding using BCD, Grey, ASCII and Unicode binary codes.

Boolean Algebra fundamentals: Axiomatic definition of Boolean algebra, the AND, OR, NOT logic operations, representation of Boolean operations using logic gates. Logic gate characteristics, IEEE / ANSI standards and conventions, derived Boolean algebra operations (NAND, NOR, XOR, XNOR), implementation and conversion between standardized Boolean function expression forms and conventions, description and effective utilization of Boolean algebra theorems, DeMorgan's Theorem.

Logic Circuits: Mathematical modelling of logic circuits using Boolean functions and truth tables, implementation of logic circuit from given Boolean function expression (logic circuit design), derivation of Boolean function from given logic circuit (logic circuit analysis), derivation of truth table from given logic circuit (simulation)

Hardware Description Language: Introduction to Hardware Description Language (HDL), necessity of using HDL in modern digital systems design, standard HDLs (VHDL / Verilog), description of the logic synthesis process using HDLs, modelling logic circuits using HDL.

Combinational Circuit Design: Basic definition of combinational circuits, description and implementation of the analytic process followed for the design of generic combinational circuits, optimisation of combinational circuits using the Boolean function manipulation methodology and the Karnaugh Map methodology (2, 3, and 4-variable cases), combinational circuit design examples.

Integrated Circuits: Description of an Integrated Circuit (IC), history and types of ICs, the TTL and CMOS logic families, technical characteristics of ICs.

Digital Arithmetic: Signed and unsigned binary numbers, representing signed binary numbers using the 2's complement method, performing basic mathematical operations using binary numbers (addition, subtraction, multiplication, division), analytic design of adder circuits, modular design of adder circuits, carry-look ahead circuit, subtractor circuit, magnitude comparator circuit, precision and overflow issues.

Standardized Logic Circuits: Description and design of standardised circuits such as decoders / encoders, multiplexers / demultiplexers, code converters, magnitude comparators and digital displays. Use of standardised logic circuits as building blocks for modular design purposes.

	<p><u>Sequential Circuit Design</u>: Introduction to circuits with memory, sequential circuits as a special type of circuits with memory, clocked operation of circuits, description and operation of basic latches (D, SR) and flip-flops (D, JK, T), propagation delay, setup time, and hold time in latches and flip-flops. Fundamental applications of circuits with memory, including serial registers, parallel register, shift registers, asynchronous (ripple) counters and synchronous counters. The synchronous counter design process.</p> <p><u>Memory</u>: Introduction to digital memory terminology, basic operation of a memory, RAM memory and types (SRAM and DRAM), ROM memory and types.</p>								
Teaching Methodology	Face- to- face								
Bibliography	<p><i>“Digital Systems: Principles &amp; Applications”</i> by Ronald J. Tocci, Neal S. Widmer, and Greg Moss</p> <p><i>“Digital Design: With an Introduction to the Verilog HDL, VHDL, and SystemVerilog”</i> by M. Morris R. Mano, and Michael D. Ciletti</p> <p><i>“Fundamentals of Digital Logic with VHDL Design”</i>, by Stephen Brown and Zvonko Vranesic</p> <p><i>“Digital Fundamentals”</i>, by Thomas L. Floyd</p> <p><i>“Digital Logic Design”</i>, by B. Holdsworth</p>								
Assessment	<table border="1" style="margin-left: auto; margin-right: auto;"> <tr> <td style="padding: 5px;">Examinations</td> <td style="text-align: center; padding: 5px;">70%</td> </tr> <tr> <td style="padding: 5px;">Assignments/Lab</td> <td style="text-align: center; padding: 5px;">20%</td> </tr> <tr> <td style="padding: 5px;">Class Participation and Attendance</td> <td style="text-align: center; padding: 5px;">10%</td> </tr> <tr> <td></td> <td style="text-align: center; padding: 5px;">100%</td> </tr> </table>	Examinations	70%	Assignments/Lab	20%	Class Participation and Attendance	10%		100%
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